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Meta-programming for Cross-Domain Tensor Optimizations

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Abstract

Many modern application domains crucially rely on tensor operations. The optimization of programs that operate on tensors poses difficulties that are not adequately addressed by existing languages and tools. Frameworks such as TensorFlow offer good abstractions for tensor operations, but target a specific domain, i.e., machine learning, and their optimization strategies cannot easily be adjusted to other domains. General-purpose optimization tools such as Pluto and existing meta-languages offer more flexibility in applying optimizations but lack abstractions for tensors. This work closes the gap between domain-specific tensor languages and general-purpose optimization tools by proposing the Tensor optimizations Meta-Language (TeML). TeML offers high-level abstractions for both tensor operations and loop transformations, and enables flexible composition of transformations into effective optimization paths. This compositionality is built into TeML’s design, as our formal language specification will reveal. We also show that TeML can express tensor computations as comfortably as TensorFlow and that it can reproduce Pluto’s optimization paths. Thus, optimized programs generated by TeML execute at least as fast as the corresponding Pluto programs. In addition, TeML enables optimization paths that often allow outperforming Pluto.

CCS Concepts  • Software and its engineering → Semantics; Source code generation; Domain specific languages; General programming languages;

Keywords  tensor algebra, meta-programming, code generation and optimization, denotational semantics

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1 Introduction

Tensors are the central data structure in many modern application domains, including scientific computing, image processing, and machine learning. Operating on tensors requires multi-dimensional and computationally intense loop nests that are difficult to optimize. Optimization strategies are complex, including not only loop transformations but also data layout and mapping decisions as well as algebraic transformations. Moreover, optimization choices frequently depend on the target platform and the application domain.

Existing frameworks such as Theano [7] or TensorFlow [2] support the development of machine learning programs that operate on tensors, but their built-in optimization strategies cannot be expected to generalize well to other applications domains. This is also the case for TACO [18], which targets applications that mix dense and sparse tensors. The state-of-the-art polyhedral loop optimizer Pluto [8] provides more flexibility in optimizing general loop nests from any application domain. However, since Pluto is a general-purpose tool, it has no abstractions for tensors, and hence suitable algebraic and layout transformations are out of its scope. Moreover, Pluto’s built-in heuristics for loop optimizations limit its flexibility in composing finely tuned sequences of transformations.

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High-level meta-programming languages and optimization APIs [4, 9, 10, 12, 13, 19, 21, 23, 39] offer more flexibility in expressing typical loop transformations, e.g., fusion, tiling, or interchange. Moreover, the level of abstraction for loop transformations both improves productivity in manual code optimization and eases the automation of searches for optimizations. However, existing languages and APIs often rely on an imperative programming style with limited flexibility in composing loop transformations. This makes it difficult to meet the needs of a wide range of tensor-based applications from different domains.

To address the lack of cross-domain solutions for optimizing programs that operate on tensors, this paper presents TeML, the Tensor optimizations Meta-Language. TeML makes tensor expressions and loop transformations first-class citizens, and it overcomes the limitations of existing solutions by embracing two key ideas. First, TeML provides high-level abstractions for characteristic tensor operations such as contraction, the outer product or the Hadamard product (i.e., entrywise multiplication). These abstractions encode algebraic information that is required to perform algebraic transformations. Second, TeML unifies the stages of program construction and transformation, both of which are specified in a functional programming style, thus rendering TeML’s language constructs fully and flexibly composable.

After giving an overview of TeML (Section 2), we present a formal specification of TeML’s semantics (Section 3), which enables clean reasoning about interactions between tensor expressions and loop transformations, and also highlights some subtleties in defining semantics for meta-languages. The power of TeML’s functional style is brought to bear when we show how TeML’s more abstract tensor operations and loop transformations can be defined in terms of more fundamental TeML constructs (Section 4). Using a set of characteristic benchmarks from a range of application domains, we demonstrate that TeML is as expressive as high-level tensor languages and that it offers the flexibility to compose loop optimizations such that better program performance can be achieved than with state-of-the-art tools (Section 5).

2 Overview

As a meta-programming language, TeML is designed to specify how programs in a target language (typically C) are to be generated and transformed. In particular, TeML specifies target language programs that operate on tensors; and such programs are generally formed of tensor expressions and loop nests. TeML is purposefully low-level, including no abstractions for more advanced mathematical structures that occur in the context of tensor algebra (e.g., co-/contravariant indices). Thus, TeML meta-programs operate only on two kinds of objects: tensor expressions and loop nests. As a consequence, TeML can be used as an intermediate language for a wide range of tensor algebra frameworks that implement more abstract structures and rewrite rules at a higher level.

According to the TeML grammar in Figure 1, a TeML meta-program is a sequence of statements. Statements include assignments of the form \( \text{id} = \text{expression} \), and an expression produces either a tensor (Texpression) or a loop (Lexpression). Identifiers (\( \text{id} \)) can refer to either of these objects.

A new tensor (scalar) is introduced by tensor (scalar). The dimensions of the new tensor are specified as a list of integers, and we subsequently refer to this list as the shape of the tensor. All scalars and elements of tensors are assumed to be of a fixed type that can be set globally for the TeML system, e.g. to a floating-point or integer type. Tensors (and scalars) are the fundamental building blocks of complex tensor expressions, which are formed with the TeML built-ins \( \text{eq}, \text{vop}, \text{and op} \). Since only identifiers are allowed as arguments, TeML specifies complex tensor expressions in a 3-address format, e.g. \( t2 = \text{vop}(t0, t1, [], []). \)

The \( \text{vop} \) built-in implements arithmetic expressions and can be either of \( \text{vadd}, \text{vsub}, \text{vmul}, \text{or vdiv} \). The optional arguments to \( \text{vop} \) are lists of iterators (\( \text{iters} \)) that are used to index tensors. For example, if \( t0, t1 \) refer to tensors of shapes \([N1, N2]\) and \([N3]\) respectively, then \( \text{vadd}(t0, t1, [[i1, i2], [i3]]) \) represents the expression \( t0[i1][i2] + t1[i3] \) in the target language. The \( \text{v} \) in \( \text{vop} \) is for virtual, indicating that the elements of the resulting tensor expression are not stored in memory, as opposed to a real tensor that is backed by memory. In the previous example, the values of \( t0[i1][i2] + t1[i3] \), for varying values of \( i1, i2, i3 \), do not reside in memory but must be computed from the elements of the real tensors \( t0 \) and \( t1 \).

The \( \text{eq} \) built-in yields an assignment in the target language, e.g., the TeML assignment \( t2 = \text{eq}(t0, [i1, i2] \rightarrow [i2, i1]) \) represents the target language assignment \( t2[i2,i1] = t0[i1,i2] \). As a consequence, \( t2 \) must refer to a real tensor that has previously been introduced by \( t2 = \text{tensor}([N2, N1]) \). The \( \text{op} \) built-in is syntactic sugar for the combination of \( \text{vop} \) and \( \text{eq} \), i.e. \( \text{op} \) assigns the value of a tensor expression (viz. a virtual tensor) to a real tensor.

The identifier \( \text{id}_1 \) in the TeML assignment \( \text{id}_1 = \text{expression} \) has two meanings. First, from TeML’s meta-programming perspective, \( \text{id}_1 \) is used to reference a tensor expression. Second, in the generated target program, \( \text{id}_1 \) is the name of the tensor variable that is assigned to. This default behavior can be overridden by using an assignment of the form \( \text{id}_1 = \theta \text{id}_2 : \text{expression} \). Then, \( \text{id}_1 \) still refers to the tensor expression in TeML, but \( \text{id}_2 \) is used in the generated target program. It is not uncommon in meta-programming that names and objects acquire multiple meanings that depend on the abstraction level under consideration.

Low-level arithmetic operations such as \( \text{add}, \text{sub}, \text{and mul} \) give TeML a high degree of expressiveness. However, to leverage the full potential of meta-programming, it is desirable to manipulate higher-level operations, e.g. tensor...
contraction, that abstract common tensor kernels such as the matrix product, the matricized tensor times Khatri-Rao product, or the sampled dense-dense matrix product. Contraction is also a specific example of a class of higher-level operations known as reductions. We defer the introduction of various high-level operations to Section 4 to avoid blowing the formal specification of the core of TeML (in Section 3) out of proportion. It is one of the strengths of TeML that complex operations can be defined compositionally from more fundamental constructs. Note, however, that virtual reduction operations, e.g. vcontract for contraction, must be considered fundamental, as is explained in Section 4.2.3. An identifier id, that refers to a tensor expression is wrapped in a loop nest by id = build(id). The identifier id then refers to this loop nest, whose depth equals the number of iterators that appear in the tensor expression id. Building the loop nest id automatically numbers the iterators that appear in the tensor expression id: iterator ik is introduced by the loop at nesting level k inside the loop nest id. The range of each iterator ik is inferred from how it is used in the tensor expression id.

The loop nest id can be transformed by stripmine, interchange, fuse_outer, fuse_inner, and unroll. Loop transformations are non-destructive and thus always return a new loop nest without affecting what id refers to. Note that build generates a perfect loop nest, but the transformations stripmine, interchange, fuse_outer, fuse_inner, and unroll may lead to non-perfectly nested loops.

Figure 1. TeML core grammar

TeML’s purpose is the specification and manipulation of programs that operate on tensors, both of which are fully decoupled from the data held by tensors. Nonetheless, to initialize individual elements of tensors, TeML provides the special init statement, details of which we omit since they are not central to the presentation of TeML. The codegen statement generates the target language code for the loops that are referenced by the arguments (ids) of codegen. Presently, code generation straightforwardly translates a TeML loop into its equivalent in C, similar to the process described in [26].

3 Formal Specification

This section gives the denotational semantics of TeML. Since TeML manipulates tensor expressions and loop nests, both of which can be represented as trees, domains of trees feature prominently in TeML’s semantics.

3.1 Domains of Trees

Trees are pairs ⟨n, ts⟩, where n is the root node and ts is a list of subtrees whose roots are the children of n. Thus, the pair ⟨n, []⟩, where [] is the empty list, is a leaf node. Figures 2 and 3 illustrate this notation of trees.

Tensor expressions are represented as trees whose nodes are triples (op, S, I), where op is an operation or identifier, S is the shape of the tensor expression (i.e. a list of its dimensions), and I is a list of iterators. Figures 4–6 give examples of such trees. Note that only identifiers, i.e. nodes (op, S, I) with op = id, may appear as leaf nodes. Internal nodes represent operations. It is not meaningful to give an iterator list for an operation, which is indicated by the special value • in Figures 4–6. Thus, the domain T of tensor expressions can be given the following recursive definition:

\[
T = \{ (\text{op}, S, I), ts \mid (ts = []) \lor (ts = [t_1, \ldots, t_k] \land t_i \in T), I \neq \epsilon \Rightarrow ts = [] \land op = id \}.
\]  

For identifiers we also allow I = ϵ, indicating that the iterator list has not been set yet. Note that ε \notin [], and also ε \neq •.

Shapes can also take the special value •. This happens only for tree nodes (ε, •) that represent assignment operations. TeML assignments do not produce tensor-valued expressions in the target language and thus have no meaningful shape (viz. dimensions) at the meta-level.

Loop nests are also represented as trees of a certain structure: the nodes are iterators and the children of a node are either loop nests or tensor expressions. Thus, the domain L′ of loop nests can be recursively defined as

\[
L′ = \{ (id, [x_1, \ldots, x_k]) \mid x_i \in L′ \cup T \}.
\]
A = tensor([N1, N2])
B = eq(A, [i1, i2] -> [i2, i1])

\(E_i[\text{build}(B)]\sigma_2 = \langle i_1, \langle i_2, \sigma_2(B) \rangle \rangle:~\)

for (int i2 = 0; i2 <= (N2-1); i2++)
for (int i1 = 0; i1 <= (N-1); i1++)
C[i2][i1] = A[i1][i2] + B[i1][i2];

Figure 4. Matrix transposition implemented with \texttt{eq}. The tree on the left depicts \(\sigma_2(B)\). The target language (C) code at the bottom results from \texttt{build}(B).

\(A = \text{tensor}(\langle N, N \rangle)\)
\(B = \text{tensor}(\langle N, N \rangle)\)
\(C = \text{tensor}(\langle N, N \rangle)\)
\(D = \texttt{@C:} \texttt{add}(A, B, \langle [i1, i2], [i1, i2] \rangle \rightarrow [i1, i2])\)

\(E_i[\text{build}(D)]\sigma_1 = \langle i_1, \langle i_2, \sigma_1(D) \rangle \rangle:~\)

for (int i1 = 0; i1 <= (N-1); i1++)
for (int i2 = 0; i2 <= (N-1); i2++)
C[i2][i1] = A[i1][i2] + B[i1][i2];

Figure 5. The \texttt{@id} construct. The tree on the left depicts \(\sigma_1(D)\). The C code on the bottom left results from \texttt{build}(D).

\(A = \text{tensor}(\langle N, N \rangle)\)
\(B = \text{tensor}(\langle N, N \rangle)\)
\(C = \text{tensor}(\langle N, N \rangle)\)
\(D = \texttt{vadd}(A, B, \langle [i1, i2], [i1, i2] \rangle)\)
\(E = \texttt{mul}(C, D, \langle [i1, i2], \rangle \rightarrow [i1, i2])\)

\(E_i[\text{build}(E)]\sigma_1 = \langle i_1, \langle i_2, \sigma_1(E) \rangle \rangle:~\)

for (int i1 = 0; i1 <= (N-1); i1++)
for (int i2 = 0; i2 <= (N-1); i2++)
E[i1][i2] = C[i1][i2] * (A[i1][i2] + B[i1][i2]);

Figure 6. More complex tensor expression, including assignment to tensor \(E\). The trees on the left depict \(\sigma_4(D)\) (top tree) and \(\sigma_5(E)\) (bottom tree) respectively. The target language (C) code on the bottom left results from \texttt{build}(E).
We now specify the behavior of statements, and how a syntactic entity manipulates a specific state

The state of a domain of loops

The trees \( \langle \bullet, [x_1, \ldots, x_k] \rangle \) represent sequences of top-level loops and tensor expressions \( x_1, \ldots, x_k \), i.e.

So far we have always referred to iterators by their names, and TeML’s naming convention for iterators was introduced in Section 2. However, the iterators that appear as nodes in the tree representation of loop nests carry additional information, namely their inferred ranges. The range of an iterator is a triple \((lb, ub, st)\) consisting of integer values \( lb \) (lower bound), \( ub \) (upper bound), and \( st \) (step).

3.2 State

The state of a TeML meta-program maps identifiers to trees representing either tensor expressions or loop nests. Thus, the domain \( S \) of states is defined as

Hence, a specific state \( \sigma \in S \) is formally a function

3.3 Valuation Functions

We now specify the behavior of TeML programs in terms of valuation functions. For each syntactic category (statement, expression etc.) there is a valuation function that defines how a syntactic entity manipulates a specific state \( \sigma \in S \):

\[ P_{\text{prog}} : \text{program} \rightarrow (S \rightarrow S) \]

\[ P_{\text{stmt}} : \text{stmt} \rightarrow (S \rightarrow S) \]

\[ E_1 : \text{Texpression} \rightarrow (S \rightarrow T) \]

\[ E_1 : \text{Lexpression} \rightarrow (S \rightarrow L) \]

\[ P_{\text{prog}}[\epsilon] = id_S \]

\[ P_{\text{prog}}[s \ p] = P_{\text{prog}}[p] \circ P_{\text{stmt}}[s] \]

Figure 7. Definition of \( P_{\text{prog}} \). \( \epsilon \) denotes an empty program, \( s \) a statement, and \( p \) a program; \( \circ \) is function composition.

Figure 4–6 also give the tree representations (underlined, in the \( \langle, [], \rangle \) notation) of the perfect loop nests returned by \texttt{build}. Non-perfect loop nests may result after applying loop transformations. While \( L' \) includes non-perfect loop nests, it does not include assignment operations at the top level of a TeML program, i.e. assignment operations not nested inside any loop. To capture such corner cases, we take as the domain of loops

\[ L = L' \cup \{ (\bullet, [x_1, \ldots, x_k]) \mid x_i \in L' \cup T \} \]

The trees \( \langle \bullet, [x_1, \ldots, x_k] \rangle \) represent sequences of top-level loops and tensor expressions \( x_1, \ldots, x_k \), i.e. loops and tensor expressions not nested inside a loop.

3.3.1 Tensor Expressions

Figure 8 defines the valuation function \( E_1 \). Given a tensor expression as its argument, \( E_1 \) constructs the tree that represents this tensor expression. The tree that is constructed for \texttt{scalar} consists of the node \( \langle \Box, [], \bullet \rangle \), which sets the shape of a scalar to the empty list [ ] and forbids iterators to index scalars since the last component of the triple is \( \bullet \). When a tree is constructed for \texttt{tensor}, the last component of the triple is left empty, indicated by \( \epsilon \) in Equation (13). A list of iterators is filled in for \( \epsilon \) when the tensor is used in an expression. The example in Figure 4 demonstrates this: the tensor referred to by \( A \) has no iterator list (in either state \( \sigma_1 \)
or \( \sigma_3 \)); but when \( A \) is used in the expression eq, an iterator list is filled in based on the arguments of eq (cf. state \( \sigma_2 \)).

The symbol \( \Box \) denotes a placeholder for an identifier, which can only be filled in once the identifier on the left-hand side of an assignment has been seen. Hence, \( \mathcal{P}_{\text{stmt}} \) (discussed below) is responsible for filling in an identifier for \( \Box \).

For eq, the function \( \mathcal{E}_t \) constructs a tree representing an assignment operation. This also uses a placeholder to defer filling in the identifier for the tensor on the left-hand side of the constructed assignment. The grammar in Figure 1 allows the iterator list \( I_0 \) to be absent, i.e. \( I_0 = \epsilon \). The where-clause after Equation (14) specifies when this is possible. If \( I_0 = \epsilon \), the identifier \( t \) must refer to a tensor expression that already has a valid iterator list \( (l' \neq \epsilon) \), and this then becomes the iterator list of \( y \) \( (l'' = l') \). If \( I_0 \neq \epsilon \), then \( t \) must refer to a tensor expression with no iterator list \( (l' = \epsilon) \), and then \( I_0 \) is filled in as the iterator list of \( y \) \( (l'' = I_0) \). The optional arguments \( I_0 \) and \( I_1 \) of \( \text{vop} \) are handled in the same way.

As pointed out in Section 2, \( \text{op} \) is syntactic sugar for \( \text{vop} \) followed by eq. Equation (16) makes this precise. The notation \( \sigma(t \mapsto x) \) means that the map \( \sigma \) is augmented with a mapping of the identifier \( t \) to \( x \). Generally, an existing mapping for \( t \) in \( \sigma \) \( (t \in \text{dom}(\sigma)) \) is overwritten. In Equation (16), however, \( t \) is chosen to be a fresh identifier not already mapped by \( \sigma \).

Note that the arguments \( I_0, I_1 \) of \( \text{op} \) are simply passed on to \( \text{vop} \) in Equation (16). The \text{mul} operation in Figure 6 is an example of the use of an empty iterator list, i.e. \( I_1 = \epsilon \).

\( \mathcal{P}_{\text{stmt}} \) produces a new state from its argument \( \sigma \) by adding a new mapping for an identifier \( \text{id} \) in Equation (17) or \( id_1 \) in Equation (18)). The target of this new mapping is an expression tree \( x' \) that is produced by first evaluating \( \mathcal{E}_t \llbracket e_i \rrbracket \) and then, in Equation (17), replacing potential occurrences of \( \Box \) with the identifier \( \text{id} \) (in symbols: \( x: \{ \text{id} / \Box \} \)). Now, \( id \) occurs in the expression tree \( x' \) and will thus make it into the target language program generated by TeML, but \( id \) is also mapped in the new state \( \sigma \{ \text{id} \mapsto x' \} \). This is the double meaning of \( \text{id} \) (cf. Section 2) made precise. Equation (18) does not lead to this double meaning since \( \Box \) is replaced with \( id_2 \) from the \( \text{\@} id_2 \) construct, of which Figure 5 gives a worked example. Generally, the right panes of Figures 4–6 give step-by-step evaluations of \( \mathcal{P}_{\text{pre}} \), broken down into evaluations of \( \mathcal{P}_{\text{stmt}} \).

Since TeML assignments have a 3-address format, nested evaluations of \( \mathcal{E}_t \) do not occur. Therefore, there is never more than one occurrence of \( \Box \) in the tree \( x \) in Equations (17), (18).

In Equation (14) the shape \( S' \) must be inferred from the properties of \( y_1 \) and \( S' \) in Equation (15) must be inferred from \( y_0, y_1 \). Shape inference for tensor expressions is straightforward and follows a strategy analogous to the typing of tensor expressions in [25]. Inference fails for malformed tensor expressions, in which case TeML cannot generate a valid target language program. Also analogous to [25], the TeML assignments in Equations (17), (18) are malformed if \( id \) or \( id_2 \), respectively, has not been introduced with \text{tensor}.\n
\[\mathcal{E}_t[\text{build}(t)] = \lambda \sigma. \text{let } r = \text{"number of iterators in } \sigma(t)\text{"} \]
\[i_k = (0, ub_k, 1) \text{ for } k = 1, \ldots, r \]
\[\text{in } \langle i_1, \ldots, (i_r, [\sigma(t)]) \rangle \ldots \]
\[\text{where } \sigma(t) = \langle (\epsilon, \bullet, \bullet), [x, y] \rangle \]

\[\mathcal{E}_t[\text{stripmine}(l, r, v)] = \]
\[\lambda \sigma. \text{let } \langle i_1, \ldots, (i_r, x) \rangle = \sigma(l) \]
\[\text{in } \langle i_1, \ldots, (i_r, x) \rangle \ldots \]

\[\mathcal{E}_t[\text{interchange}(l, r_1, r_2)] = \]
\[\lambda \sigma. \text{let } \langle i_1, \ldots, (i_r, x) \rangle = \sigma(l) \]
\[\text{in } \langle i_1, \ldots, (i_r, x) \rangle \ldots \]

\[\mathcal{E}_t[\text{fuse_outer}(l_1, l_2, r)] = \]
\[\lambda \sigma. \text{let } \langle i_1, \ldots, (i_r, x) \rangle = \sigma(l_1) \]
\[\text{in } \langle i_1, \ldots, (i_r, x) \rangle \ldots \]

\[\mathcal{E}_t[\text{fuse_inner}(l, r)] = \]
\[\lambda \sigma. \text{let } \langle i_1, \ldots, (i_r, x) \rangle = \sigma(l) \]
\[\text{in } \langle i_1, \ldots, (i_r, x) \rangle \ldots \]

\[\mathcal{E}_t[\text{unroll}(l, r)] = \]
\[\lambda \sigma. \text{let } \langle i_1, \ldots, (i_r, x) \rangle = \sigma(l) \]
\[\text{in } \langle i_1, \ldots, (i_r, x) \rangle \ldots \]

\[\mathcal{E}_{\text{stmt}}[\text{id} = e_i] = \lambda \sigma. \text{let } x = \mathcal{E}_t[\llbracket e_i \rrbracket] \text{ in } \sigma \{ \text{id} \mapsto x \} \]

\[\text{Figure 9. Valuation functions } \mathcal{E}_t \text{ and } \mathcal{P}_{\text{stmt}} \text{ for loop expressions. The symbol } || \text{ denotes concatenation of lists.} \]

3.3.2 Loop Expressions

The definitions of \( \mathcal{E}_t \) for loop expressions in Figure 9 freely identify iterators with triples \( (lb, ub, st) \) that specify iterator ranges (cf. the last paragraph of Section 3.1). When a loop nest is built around a tensor expression in Equation (19), the upper bounds \( ub_t \) are inferred based on the positions in which iterators are used to index tensors in the expression \( \sigma(t) \). Note that the where-clause after Equation (19) enforces that loop nests are only built around assignments of tensors. Example evaluations of \( \mathcal{E}_t[\text{build}(id)] \) appear (underlined) in the bottom left corners of Figures 4–6, together with the corresponding loop nests in the target language, i.e. C.
Equation (20) implements a version of stripmining that does not transform tensor indices inside tensor expressions. The definition of \textit{interchange} in Equation (21) is straightforward: it swaps iterators. Note that \textit{fuse\_outer} in Equation (22) fuses identical outer iterators of two loop nests, and \textit{fuse\_inner} in Equation (23) fully fuses loops at nesting level \( r \). The tree patterns on the left-hand sides in the let-expressions in Equations (22) and (23) enforce that the loop nests \( \sigma(l_1), \sigma(l_2), \) and \( \sigma(l) \) have specific shapes that are required for fusion to be possible.

The definition for \textit{unroll} in Equation (25) is standard. Note that unrolling the outermost loop, i.e. \( r = 1 \) in Equation (25), generally results in a sequence of loops and tensor expressions that are not nested inside another loop and thus has the form \( \langle \bullet, \left[ x_1, \ldots, x_k \right] \rangle \in \mathbb{L} \) (cf. Equation (3)).

The definition of \( \mathcal{P}_{\text{stmt}} \) in Equation (26) is completely standard. No placeholders for identifiers are required for loop expressions. This is ultimately because loops have no meaningful names at the level of target language programs.

### 3.3.3 Parallelization

The TeML core language (cf. Figure 1) can be extended with the loop expressions \textit{parallelize} and \textit{vectorize} that specify that a loop be parallelized or vectorized, respectively, using pragmas or intrinsic functions. Our representation of loops as trees in \( \mathbb{L} \) does not capture this, which is not a problem since neither parallelization nor vectorization changes the structure of a loop. Further work is required to enable more fine-grained control over parallelization, including for instance the generation of atomic sections or optimized parallel reductions. This would necessitate extending both TeML’s expressiveness and its code generation process.

### 3.4 Type Safety in TeML

We have defined the valuation functions in a \( \lambda \)-calculus with a primitive tree type that has the constructor \( \langle, [], [] \rangle \). In the let-expressions, we have made extensive use of pattern matching against this constructor. Additional constraints are enforced by where-clauses. Whenever pattern matching fails or a where-clause is not satisfied, the TeML program that is being evaluated is malformed. A TeML program is also malformed when shape inference fails or when real tensors are assigned to that have not previously been introduced with \texttt{tensor}, as discussed at the end of Section 3.3.1.

TeML detects whether a program is malformed while the program is being evaluated. This amounts to dynamic type checking, where \textit{dynamic} means during the execution of a meta-program, i.e. before a target language program has even been generated.

### 4 Compositional Definitions

In this section, we extend TeML with additional loop and tensor expressions that implement more abstract operations on loops and tensors. These abstract operations enhance TeML’s expressiveness and, from the perspective of a TeML implementation, are considered built into the language. Nonetheless, the key observation in this section is that more abstract operations can be defined in terms of the TeML core language by means of the composition in Equation (11). This facilitates a modular implementation of TeML and also demonstrates the flexibility of the language.

#### 4.1 Loop Transformations

The loop transformations in the TeML core language, as defined in Figure 9, appear to be rather restrictive. For example, \textit{interchange} only operates on immediately adjacent loops, and \textit{unroll} performs complete unrolling of a loop. Using loop tiling as an example, we now demonstrate how common, more flexible loop transformations can be composed from the ones in the core language. In order to increase data locality, tiling organizes the iteration space of a loop nest into blocks. This can be achieved by multiple applications of stripmining, each of which introduces blocks into a single loop, cf. Equation (20). The auxiliary transformation \textit{stripmine\_n} expands into the multiple applications of stripmining required for tiling.

![Equation (27)](image)

Note that if \( d \) is the depth of the original (perfect) loop nest \( l \), then the depth of the resulting loop nest \( l' \) is \( d + n \).

To arrange the new loops that stripmining has introduced into the right order for tiling, several interchanges are needed. To this end, we introduce \textit{interchange\_n} that permutes an iterator \( i \) in a loop nest through the next \( n \) iterators (towards the innermost loop),

![Equation (28)](image)

Finally, TeML’s loop transformation \textit{tile} can be defined,

![Equation (29)](image)
where $d$ is the depth of the original loop nest $I$, and the resulting loop nest $I'$ has depth $2d$. Figure 10 demonstrates how tile handles a loop nest of initial depth 3. First, stripmining introduces the additional iterators $i'_2$, $i'_4$, and $i'_5$ in Figure 10b. Then, three applications of interchange_n move these iterators towards the deep positions in the loop nest, cf. Figure 10c.

Other well-known loop transformations, e.g. partial unrolling and unroll-and-jam, can be composed from the transformations defined in Figure 9 in a similar fashion.

4.2 Tensor Operations

Tensor operations that are more abstract than the fundamental arithmetic operations, such as add and mul, allow algorithms to be expressed more concisely in TeML. In particular, more abstract operations largely hide the explicit, and therefore error-prone, manipulation of iterator lists. As was the case for loop transformations, abstract tensor operations can be defined in terms of TeML’s core operations, requiring only a few simple side rules for iterator lists and shapes that a TeML implementation can easily check.

4.2.1 Entrywise Operations

If the tensors $t_0$ and $t_1$ have the same shapes, the core arithmetic operations add, sub, mul, and div can be applied to all entries (viz. components) of $t_0$ and $t_1$ simultaneously. Let $r$ denote the length of the shapes of $t_0$ and $t_1$. The entrywise application of arithmetic operations is enforced in the following definitions by the fact that the same iterator list $I = [i_1, \ldots, i_r]$ is used for both $t_0$ and $t_1$.

$$
E[I][\text{entrywise}_{op}(t_0, t_1)] = E[I][\text{op}(t_0, t_1, [I, I])].
$$

$$
E[I][\text{entrywise}_{op}(t_0, t_1)] = E[I][\text{op}(t_0, t_1, [I, I] \rightarrow I)].
$$

Theses formulae and their implementations in TeML generalize to arbitrary numbers of arguments.

4.2.2 Transposition

Transposition amounts to reordering an iterator list:

$$
E[I][\text{transpose}(t, [r_1, r_2], \ldots, [r_{2k-1}, r_{2k}])] = E[I][\text{eq}(t, I \rightarrow I')].
$$

where $I$ is a list of fresh iterators for $t$, and $I'$ is obtained from $I$ by swapping pairs of iterators at positions $p_{2i-1}$ and $p_{2i}$, for $i = 1, \ldots, k$. Since transpose is defined in terms of eq, it results in an assignment to a real tensor in the target language program. The corresponding virtual operation vtranspose does not introduce any operations into the target language program, it simply causes TeML to reorganize iterator lists.

4.2.3 Contraction

Contraction is central to many complex algorithms that operate on tensors. In particular, the dot product and matrix multiplication are low-dimensional instances of contraction.

Let $S_0$ and $S_1$ be the shapes of tensors $t_0$ and $t_1$, and let these shapes have lengths $s_0$ and $s_1$ respectively. The tensors $t_0$ and $t_1$ can be contracted along dimensions $r_0$ and $r_1$, respectively, if the $r_0$-th entry of $S_0$ equals the $r_1$-th entry of $S_1$. Then,

$$
\mathcal{P}_{\text{stmt}}[t'] = \text{contract}(t_0, t_1, [r_0, r_1]) = \mathcal{P}_{\text{prog}}[t_2 = \text{vmul}(t_0, t_1, [I, I]), \ t' = \text{add}(t', t_2, [I', I] \rightarrow I')].
$$

where $I = [i_0, \ldots, i(r_0 - 1), k, i(r_0 + 1), \ldots, i_{s_0}]$, $J = [j_0, \ldots, j(r_1 - 1), k, j(r_1 + 1), \ldots, j_{s_1}]$, $I' = ([I \setminus k] || (J \setminus k))$.

The iterator $k$ appears at position $r_0$ in $I$, and at position $r_1$ in $J$; it no longer appears in the iterator list $I'$ for the resulting tensor $t'$. Contraction of more than one pair of dimensions is defined analogously, but the formulae become unwieldy.

Contraction is an example of a reduction operation and therefore crucially requires an accumulator. In Equation (33), the resulting tensor $t'$ also acts as the accumulator, and thus $t'$ appears both as the result and the argument of the add operation. Because of the add in Equation (33), $t'$ is a real tensor, which in the target language program is backed by memory. This explains why the virtual counterpart vcontract cannot be given a definition in terms of more fundamental TeML operations: in a virtual operation, TeML does not have at its disposal a real tensor that can play the role of the accumulator. Hence, vcontract is considered fundamental in TeML, as already pointed out in Section 2.

Analogous statements hold for other reduction operations that TeML can be extended with by suitable compositional definitions.

4.2.4 Outer Product

The outer product, often simply referred to as the tensor product, combines the components of tensors $t_1, \ldots, t_n$ into a single tensor whose shape then is the concatenation of the shapes of $t_1, \ldots, t_n$. In formulae,

$$
\mathcal{P}_{\text{stmt}}[t'] = \text{outerproduct}(t_1, \ldots, t_n) = \mathcal{P}_{\text{prog}}[s_{n-1} = \text{vmul}(t_{n-1}, t_n, [I_{n-1}, I_n]) \ldots s_2 = \text{vmul}(t_2, s_3, [l_2, \epsilon]) t' = \text{vmul}(t_1, s_2, [l_1, \epsilon] \rightarrow I')].
$$
As this section unfolds, it will become clear that not all kēr-
TeML
The latter is the equivalent of
Table 1 describes a range of common tensor kernels from
16.04 operating system. The generated C programs (either
I
also low-level operations, i.e. einsum
5.1 Expressing Tensor Computations
We report the relative speed-ups of Pluto’s best optimization
sequences of
TeML
5.2 Reproducing Pluto’s Optimization Paths
For the tensor kernels from Table 1 we have identified
fastest program variants that can be generated with Pluto
by manipulating its heuristics for loop fusion, tiling, inter-
change, vectorization, and thread parallelism. Table 2 lists
the TeML equivalents of the loop optimization paths that
cause Pluto to generate the fastest programs. Note that
the TeML transformations vectorize and parallelize have
been implemented with compiler-specific pragmas for vec-
torization and OpenMP pragmas for thread parallelization.

The stencil kernel blur is not included in Table 2 since
Pluto’s best optimization path for this kernel performs loop
skewing, which cannot yet be expressed in TeML. Also, as
standard matrix multiplication has been thoroughly studied,
cia. [34] in particular, our analysis focuses on bmm, which
presents a less conventional computation pattern.

Since the sequences of TeML loop transformations in Table 2 reproduce the effects of Pluto’s optimizations, C pro-
grams generated either from TeML or Pluto for the kernels
listed in Table 2 have equal execution times. For space rea-
sons we have omitted plots of these execution times since
they would only show relative speed-ups of 1.0x (within
measurement accuracy) between Pluto and TeML.

5.3 Performance Comparison
We now study opportunities for composing optimization
paths with TeML that lead to generated C programs that
outperform programs generated with Pluto. For complete-
ness we also indicate the performance of the corresponding
TensorFlow kernels. Figure 11 shows speed-ups relative to
the sequential C implementation that is the starting point for
applying optimizations with Pluto. Speed-ups are shown for
the best program variants generated with Pluto and TeML,
and for TensorFlow kernels whenever they exist (cf. Table 1).

Pluto’s best variants for mttkrp, bmm, and sādmm still offer
opportunities for data transposition, which can significantly

\[ \mathcal{P}_{\text{eq}}(s) = \mathcal{P}_{\text{vouterproduct}}(I_1, \ldots, I_n) \]

where \( I' = I_1 || \ldots || I_n \), and the iterator lists \( I_1, \ldots, I_n \) are
pairwise disjoint, so that no iterator appears more than once
in the concatenated list \( I' \). Note that the implementation of
outerproduct is not quite as modular as Equation (35)
suggests: to construct the final iterator list \( I' \), the iterator lists
\( I_1, \ldots, I_n \) from the definition of vouterproduct are needed.

5 Evaluation of TeML
Table 1 describes a range of common tensor kernels from
different application domains. These kernels serve as bench-
marks for our evaluation of TeML that assess three different
aspects of the language:
1. The capability to express tensor computations effi-
ciently. Here we compare with TensorFlow [2], whose
abstractions for tensor expressions are similar to
TeML’s tensor operations from Section 4.2.
2. The ability to reproduce loop optimization paths of
existing tools. It is natural to compare with Pluto [8],
which gives access to powerful polyhedral-based trans-
formations through an interface allowing to enable or
disable transformation heuristics.
3. The ability to easily extend optimization paths by com-
posing with additional transformations, leading to the
generation of C programs that outperform the ones
generated with Pluto.

As this section unfolds, it will become clear that not all kēr-
ners from Table 1 can be meaningfully used in the evaluations
of all of the three aspects.

All experiments reported in this section were performed
on an Intel(R) Core(TM) i7-4910MQ CPU (2.90GHz, 8 hy-
prethreads, 8192KB of shared L3 cache) running the Ubuntu
16.04 operating system. The generated C programs (either
TeML or Pluto) were compiled with the Intel C compi-
ler ICC 18.02 using the flags -O3 -xHost -qopenmp. We
used Pluto version 0.11.4, and TensorFlow version 1.6 with
support for AVX, FMA, SSE, and multi-threading.

5.1 Expressing Tensor Computations
Most benchmark kernels can be implemented using TeML’s
more abstract tensor operations, which keeps the kernel code
short. As shown in Table 1, TensorFlow and TeML programs
are of comparable sizes in terms of lines of code (LOC).

Most TensorFlow kernels use either einsum or tensordot.
The latter is the equivalent of TeML’s contract. Tensor-
Flow’s einsum operation is more low-level than tensordot
and can thus be used when the semantics of tensordot
are too restrictive, e.g. in batched matrix multiplication. In
TeML, the same functionality can be implemented using
also low-level operations, i.e. add and mul. Batched matrix
multiplication (\( C = AB \)) must be built with more low-level
operations in both frameworks due to the batch index \( b \):
\[ C[b][i][j] += A[b][i][k] * B[b][k][j] \]

Note that einsum is not flexible enough to implement all
kernels that can be expressed in TeML, as evidenced by the
convolution kernel gconv and the stencil kernel blur in Ta-
ble 1. TensorFlow does also not have a dedicated construct
for outer products, but its documentation explicitly recom-
ends using einsum for this purpose.1

In summary, practically all of TensorFlow’s constructs for
tensor expressions have equivalents in TeML that can be
used as effectively. TeML also offers support for expressing
computations that cannot be implemented in TensorFlow.

1https://www.tensorflow.org/api_docs/python/tf/einsum
2Note that Pluto offers only limited supported for unrolling heuristics.
Table 1. Tensor kernels implemented in TensorFlow and TeML. Application domains: Linear Algebra (LA), Deep Learning (DL), Machine Learning (ML), Data Analytics (DA), Fluid Dynamics (FD), Image Processing (IP).

<table>
<thead>
<tr>
<th>Matrix Multiplication</th>
<th>Name</th>
<th>Domain</th>
<th>TensorFlow Constructs used</th>
<th>TeML Constructs used</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>mm</td>
<td>LA</td>
<td>matmul</td>
<td>contract</td>
</tr>
<tr>
<td></td>
<td>tmm</td>
<td>DL</td>
<td>matmul:transpose=True</td>
<td>transpose, contract</td>
</tr>
<tr>
<td></td>
<td>bmm</td>
<td>N/A</td>
<td>einsum</td>
<td>mul, add</td>
</tr>
<tr>
<td></td>
<td>gconv</td>
<td>N/A</td>
<td>Not implemented.</td>
<td>vmul, add</td>
</tr>
<tr>
<td>Matricized Tensor Times</td>
<td>mtikr</td>
<td>DA</td>
<td>einsum or tensordot, multiply</td>
<td>vcontract, contract</td>
</tr>
<tr>
<td>Khatri-Rao product</td>
<td>sddmm</td>
<td>ML</td>
<td>einsum or tensordot, multiply</td>
<td>vcontract, contract</td>
</tr>
<tr>
<td>Sampled Dense-Dense</td>
<td>interp</td>
<td>FD</td>
<td>einsum or tensordot</td>
<td>contract</td>
</tr>
<tr>
<td>Matrix Product</td>
<td>helm</td>
<td>N/A</td>
<td>Required division</td>
<td>contract, outerproduct, div, entrywise_mul</td>
</tr>
<tr>
<td>Interpolation [15]</td>
<td>blur</td>
<td>IP</td>
<td>No stencil support.</td>
<td>op, vop</td>
</tr>
<tr>
<td>Coarsity [15]</td>
<td>coars</td>
<td>IP</td>
<td>einsum or multiply, subtract</td>
<td>ventrywise_mul, entrywise_sub</td>
</tr>
</tbody>
</table>

Table 2. Equivalents of Pluto’s optimization paths in TeML. Kernel data sizes in parentheses.

<table>
<thead>
<tr>
<th>mttkrp (250<em>250</em>250)</th>
<th>sddmm (4096*4096)</th>
<th>bmm (8192<em>72</em>26)</th>
<th>gconv (32<em>32</em>32<em>32</em>77)</th>
<th>interp (50000<em>7</em>7*7)</th>
<th>helm (5000<em>13</em>13*13)</th>
<th>coars (4096*4096)</th>
</tr>
</thead>
<tbody>
<tr>
<td>parallelize(l, 1)</td>
<td>interchange(l, 2, 3), parallelize(l, 1), vectorize(l, 3)</td>
<td>tile(l, 32), interchange(l, 7, 8), parallelize(l, 1), vectorize(l, 8)</td>
<td>interchange(l, 1, 4, 5), interchange(l, 1, 5, 6), parallelize(l, 1, 9), parallelize(l, 2, 1), vectorize(l, 2, 9)</td>
<td>interchange(l, 1, 4, 5), vectorize(l, 1, 5), interchange(l, 2, 4, 5), vectorize(l, 2, 5), parallelize(l, 1, 1), parallelize(l, 2, 1), vectorize(l, 1, 2), vectorize(l, 2, 3), vectorize(l, 3, 4)</td>
<td>fuse_outer(l, 4, 5, 6, 5), parallelize(l, 1, 1), parallelize(l, 2, 1), parallelize(l, 3, 1), parallelize(l, 4, 1), parallelize(l, 5, 1), parallelize(l, 6, 1), parallelize(l, 7, 1)</td>
<td>tile(l, 32), parallelize(l, 1), vectorize(l, 4)</td>
</tr>
</tbody>
</table>

The loop interchange $j \leftrightarrow l$ would eliminate the column-major access of $D$, and $j \leftrightarrow k$ would eliminate that of $C$. Both column-major accesses cannot be eliminated at the same time (without negatively affecting the access patterns of $A$ and $B$). Pluto chooses to interchange $j \leftrightarrow k$ (cf. Table 2), which only eliminates the column-major access of $C$. The following TeML meta-program implements $mttkr$ and resolves the column-major access of $D$ by means of data transposition, yielding a speed-up of 1.74× compared to Pluto, with the cost of transposition included (Figure 11a).

For $bmm$, however, when executing on more than two cores, the copy overhead that results from the transposition loop is greater than the cost of transposed data accesses, making it difficult to outperform Pluto (Figure 11b). Similarly, an additional transposition makes $sddmm$’s execution about 2× slower. When TeML does not apply the additional transposition to the $bmm$ and $sddmm$ kernels, performance is on par with the generated Pluto program as explained in Section 5.2. This shows that initially appealing transformations do not always guarantee a gain in performance.

It is known that BLAS implementations can outperform code generated with Pluto thanks to manually implemented optimizations that build on a detailed understanding of the underlying hardware. Some of the relevant transformations are readily available in TeML (e.g. loop tiling). As TeML is

Note that previous work [34] produced similar results for standard matrix multiplication and Pluto.

A = contract(tmp, C, [2, 1])
1d = build(Dt)
1 = build(A)
11 = parallelize(1, 1)
12 = interchange(11, [2, 3])
Meta-programming for Cross-Domain Tensor Optimizations

... TensorFlow is targeted at the machine learning domain, it...: computations are expressed using combinators, and rewrite rules are used to transform programs. The meta-programming tools Clay [4], URUK [12], CHILL [9], and Loo.py [19] rely largely on the polyhedral formalism, which focuses on the representation, analysis, and transformation of loops. However, none of these tools have semantics that model loop transformations. To the best of our knowledge,
TeML is the first (meta-)language that comes with denotational semantics for both tensor computations and explicit transformations thereof.

7 Summary and Outlook

We have presented TeML, a meta-language for programming and optimizing tensor computations. TeML offers similar levels of abstraction for expressing tensor computations as existing tensor languages, e.g. TensorFlow and Theano. Additionally, loop transformations are first class citizens in TeML, enabling meta-programming of optimization schemes comparable to and better than those of Pluto, a state-of-art polyhedral compiler, in only a few lines of code. The semantics of TeML have been formally specified in a denotational style, exhibiting a high level of compositionality as more abstract tensor operations and loop transformations can be formally defined by composition of more fundamental ones.

In the future, it would be interesting to augment TeML with abstractions for memory virtualization and mapping in order to handle, e.g., NUMA and distributed memory placement, data alignment, and padding. The semantics would have to be extended accordingly.

Note that TeML currently has no notion of whether loop transformations preserve the semantics of target language programs. This is on purpose: past research on affine transformations [5, 14, 37] has highlighted advantages of allowing non-semantics-preserving transformations as intermediate steps in optimizing programs so long as the full optimization process, once completed, retains the original meanings of programs. Extending TeML with facilities for both requiring and guaranteeing that certain transformations preserve program semantics is an interesting direction for future work.

While the present work has focused on the design of TeML as a meta-language, future work could assess the possibility of using TeML to identify platform-independent or portable optimization strategies, targeting also GPU platforms.

Finally, to enhance the usability of TeML in certain application domains, future work should aim at identifying suitable high-level abstractions for stencil patterns and general convolutions. In a similar vein, options for supporting sparse tensors might be worth investigating.

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